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CONFIRMATION NO. 3348

37490  
 CARPENTER & KULAS, LLP  
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 PALO ALTO, CA 94303

## UPDATED FILING RECEIPT



\*OC000000011810407\*

Date Mailed: 02/02/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Amit Ramchandran, San Jose, CA;

## Assignment For Published Patent Application

QuickSilver Technology, Inc., San Jose, CA;

## Domestic Priority data as claimed by applicant

This appln claims benefit of 60/422,063 10/28/2002

## Foreign Applications

If Required, Foreign Filing License Granted: 10/23/2003

Projected Publication Date: 05/13/2004

Non-Publication Request: No

Early Publication Request: No

\*\* SMALL ENTITY \*\*

## Title

Cache for instruction set architecture

**Preliminary Class**

711

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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